

GP2751

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Mailloux, et al.

Serial No.: 08/650,719

Filed: May 20, 1996

For: "ASYNCHRONOUSLY-ACCESSIBLE
MEMORY DEVICE WITH MODE SELECTION
CIRCUITRY FOR BURST PIPELINED OPERATION"



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RESPONSE TO OFFICE ACTION OF FEBRUARY 17, 1999

**Assistant Commissioner for Patents
Washington, D.C. 20231**

Certificate of Mailing (37 C.F.R. § 1.8)

Dear Sir:

In response to the Office Action of February 17, 1999, please consider the following remarks.

In the Claims:

Please amend Claims 1, 33 and 50 as indicated.

- -1. (Twice Amended) An asynchronously-accessible storage device comprising:
mode circuitry configured to select between a burst mode and a pipelined mode; and
[pipelined/burst] circuitry operable in either a burst mode or a pipelined mode coupled to the
mode selection circuitry and configured to switch between the pipelined mode and the burst mode for
operating the asynchronously-accessible storage device in either mode.